

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	384671	liquid crystal display or LCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
L11	32944	source adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
L12	162911	voltage near3 driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
L13	23846	gate adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
L14	919994	scan\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
L15	723926	invert\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
L16	862057	buffer\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
L17	270	L10 and L11 and L12 and L13 and L14 and L15 and L16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
L19	270	L10 and L11 and L12 and L13 and L14 and L15 and L16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:46
L20	18333	(chip adj on adj glass\$3 or COG or LCOS)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:48
L21	37	19 and 20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:48

S1	20	chip adj on adj glass\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:47
S2	15574	chip\$3 near6 glass\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 14:48
S3	372784	liquid crystal display or LCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:17
S4	20821	LCD panel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:18
S5	32049	source adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:19
S6	158995	voltage near3 driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:18
S7	23100	gate adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:20
S8	2233	panel adj wir\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:21
S9	900057	scan\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:21
S10	1428	contrast adj voltage\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:24
S11	21144	charge adj pump\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:25

S12	386	buffer circuit same stabiliz\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:14
S13	130629	CMOS or Complementary Metal Oxide Semiconductors	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:27
S14	12027	(CMOS or Complementary Metal Oxide Semiconductor\$3) adj inverters	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:21
S15	108421	resistance value	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:50
S16	2410	LCOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:51
S17	1040	S3 and S5 and S6 and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:51
S18	17	S3 and S5 and S6 and S7 and S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:04
S19	12	S3 and S5 and S6 and S7 and S8 and S9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:16
S20	1	S3 and S5 and S6 and S7 and S8 and S9 and S10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:12
S21	0	S3 and S5 and S6 and S7 and S8 and S9 and S16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:11
S22	2	S3 and S5 and S6 and S7 and S8 and S9 and S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:13

S23	1	S3 and S5 and S6 and S7 and S8 and S9 and S12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:14
S24	1	S3 and S5 and S6 and S7 and S8 and S9 and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:14
S25	2899	buffer circuit and stabiliz\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:15
S26	1	S3 and S5 and S6 and S7 and S8 and S9 and S25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:22
S27	130294	buffer\$6 and stabiliz\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:15
S28	1	S3 and S5 and S6 and S7 and S8 and S9 and S27	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:16
S29	1	S3 and S5 and S6 and S7 and S8 and S9 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:16
S30	46442	(CMOS or Complementary Metal Oxide Semiconductor\$3) and invert\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:22
S31	1	S3 and S5 and S6 and S7 and S8 and S9 and S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:22
S32	713471	invert\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:22
S33	8	S3 and S5 and S6 and S7 and S8 and S9 and S32	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:26

S34	843382	buffer\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:23
S35	4	S3 and S5 and S6 and S7 and S8 and S9 and S32 and S34	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:46